

PATENT & TRADEMARK OFFICE
Attorney's Docket No. ITW 7188.64

Handwritten signature and a dollar sign (\$).

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of : James M. Thommes
Application No.: 10/774128
Group Art Unit: 2838
Filed: February 5, 2004
For: Method And Apparatus For Receiving A Universal Input Voltage
In A Welding Power Source
Examiner: Shaw, C.

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

**TRANSMITTAL OF INFORMATION DISCLOSURE
STATEMENT BEFORE MAILING DATE OF EITHER
A FINAL ACTION OR A NOTICE OF ALLOWANCE**

**TIME OF TRANSMITTAL OF ACCOMPANYING
INFORMATION DISCLOSURE STATEMENT**

1. The information disclosure statement transmitted herewith is being filed **after** three months of the filing date of this national application or the date of entry of the national stage as set forth in § 1.491 in an international application or after the mailing date of the first Office action on the merits, whichever event occurred last but before the mailing date of either:
 - (1) a final action under Section 1.113 or
 - (2) a notice of allowance under Section 1.311, whichever occurs first.

CERTIFICATE OF MAILING/TRANSMISSION (37 CFR 1.8a)

I hereby certify that this correspondence is, on the date shown below, being:

deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to the Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450.

Date:

Feb. 9, 2005

Handwritten signature of George R. Corrigan

George R. Corrigan

02/14/2005 FHEK11 00000016 10774128

01 FC:1806

180.00 DP

CERTIFICATION OR FEE

2. Accompanying this transmittal is the fee set forth in 37 C.F.R. 1.17(p) for submission of an information disclosure statement under Section 1.97(c). s (\$180.00).

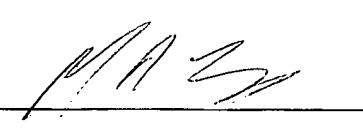
FEE PAYMENT

3. Applicant elects the option to pay the fee set forth in 37 C.F.R. 1.17(p) for submission of an information disclosure tatement under Section 1.97(c) (\$180.00).

Fee due = \$ 180.00

METHOD OF PAYMENT OF FEE

4. Please charge the fee of 180.00 and any additional fees that may be due, to the credit card specified on the attached sheet.



George R. Corrigan
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: James M. Thommes

Serial No. : 10/774,128

Filing Date : February 5, 2004

For : Method And Apparatus For Receiving A
Universal Input Voltage In A Welding
Power Source

Attorney Docket No. : ITW 7188.62

Disclosure of Prior Art, Litigation, and Litigation Documents

Honorable Commissioner of
Patents and Trademarks
Washington, D. C. 20231

Dear Sir:

Enclosed is a form 1449 and cited art.

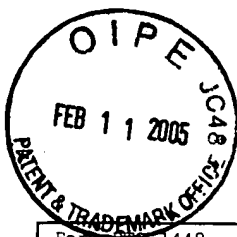
Also, Applicant is hereby notifying the Office that at least some of the subject matter being claimed is the subject of an existing litigation (Case No. 1:03-CV-00966, ED Wis), wherein the assignee Illinois Tool Works Inc. has sued Thermal Dynamics Corporation. Applicant is including copies of papers from that litigation, including:

Thermal Dynamics' Supplemental Answers To Illinois Tool Works Inc. And Miller Elec. Mfg. Co.'s Interrogatories Nos. 3-6, 8 And 13-14

These documents are being provided because the Defendant has asserted the parent patents are invalid.

Respectfully Submitted

George R. Corrigan, Reg. No. 34,803
Corrigan Law Office
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Appleton, WI 54915
(920) 954-1099



Sheet 1 of 1.

Form PTO-1449		U.S. Department of Commerce Patent And Trademark Office		ATTY. DOCKET NO. ITW 7188.64		SERIAL NO. 10/774128	
SUPPLEMENTAL INFORMATION DISCLOSURE CITATION				APPLICANT James M. Thommes			
				FILING DATE February 5, 2004		GROUP 2838	
U.S. PATENT DOCUMENTS							
EXAM. INIT.	REF	PATENT NUMBER	DATE	NAME	CLASS	SUB- CLASS	FILING DATE
		4,493,040	1/8/85	Vanderhelst			
		4,521,672	6/4/85	Fronius			
		5,345,375	9/6/94	Mohan			
		5,444,356	8/22/95	Reynolds et al.			
		5,563,777	10/8/96	Miki et al.			
FOREIGN PATENT DOCUMENTS							
EXAM INIT	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION YES NO
		JP05111766	07/05/93	Japan			
OTHER DOCUMENTS (Including Author, Title, Date, Pages, Etc.)							
		Design Issues For a Zero-Voltage-Switched Power Factor Correction Circuit and DC/DC Converter Power By: Y.V. Panov et al. 1993 VPEC Proceedings of the Virginia Power Electronics Seminar, Blackburg, VA Sept. 19-21, 1993 PP 213-224					
		High Power Factor Switching Preregulator Design Optimization Lloyd Dixon Unitrode Corporation					
EXAMINER				DATE CONSIDERED			
EXAMINER: Initial if citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							

CERTIFICATE OF MAILING/TRANSMISSION (37 CFR 1.8a)

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505 37,2003
Feb 9, 2004

George R. Corrigan